

Fig. 2

300 /

	302		304		
Channel 1	Policing Parameters (e.g. $TAT_1, I_1, L_1$ )	$R_1$	$B_1$	$V_1$	
Channel 2	Policing Parameters (e.g. $TAT_2, I_2, L_2$ )	$R_2$	$B_2$	$V_2$	
Channel 3	Policing Parameters (e.g. $TAT_3, I_3, L_3$ )	$R_3$	$B_3$	$V_3$	
•	•				
•	•				
•	•				
Channel n	Policing Parameters (e.g. $TAT_n, I_n, L_n$ )	$R_n$	$B_n$	$V_n$	

Fig. 3

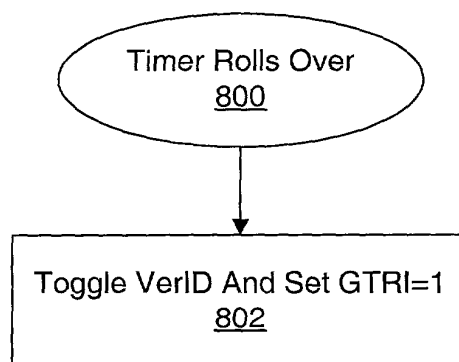


Fig. 8

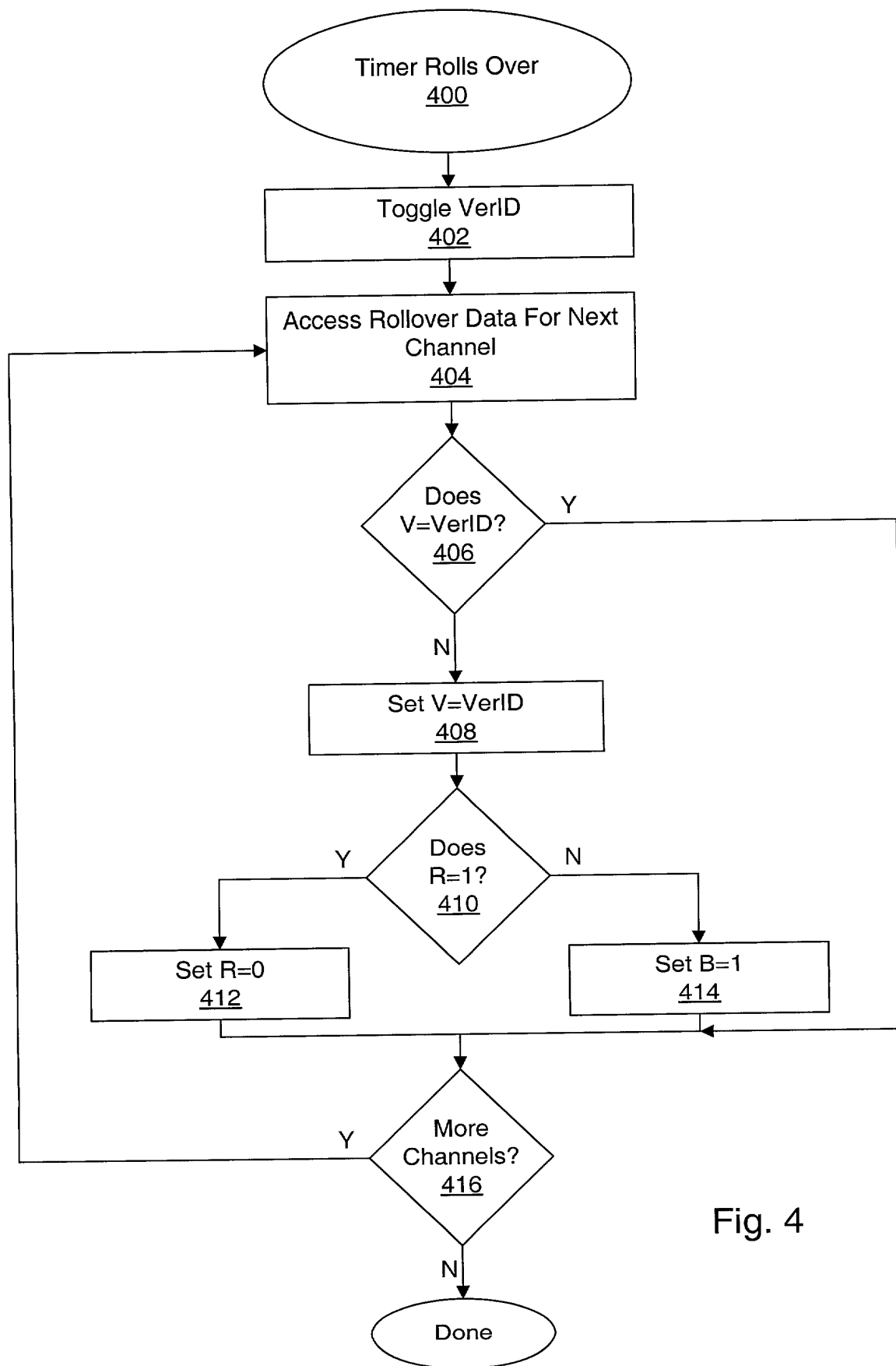


Fig. 4

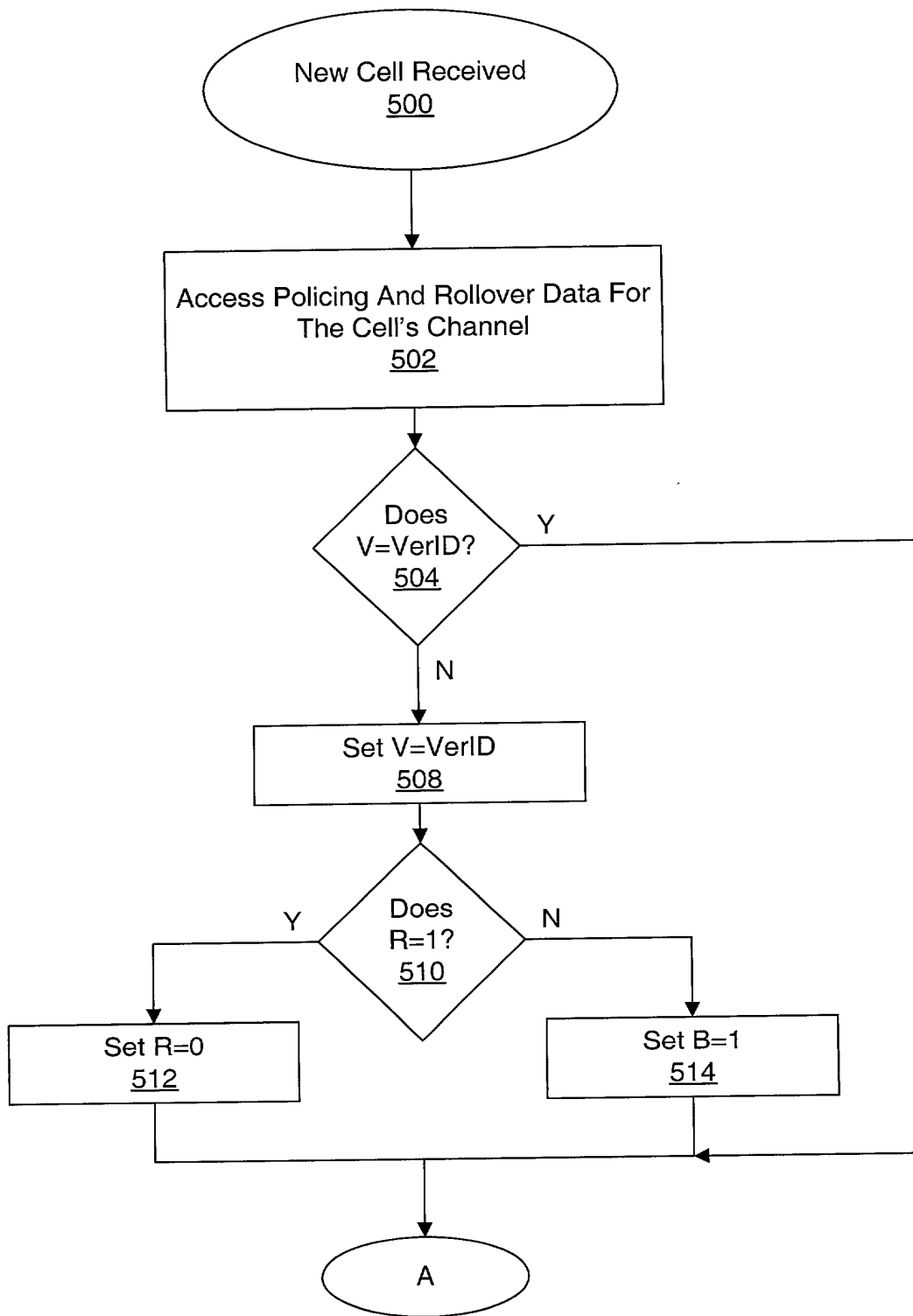


Fig. 5A

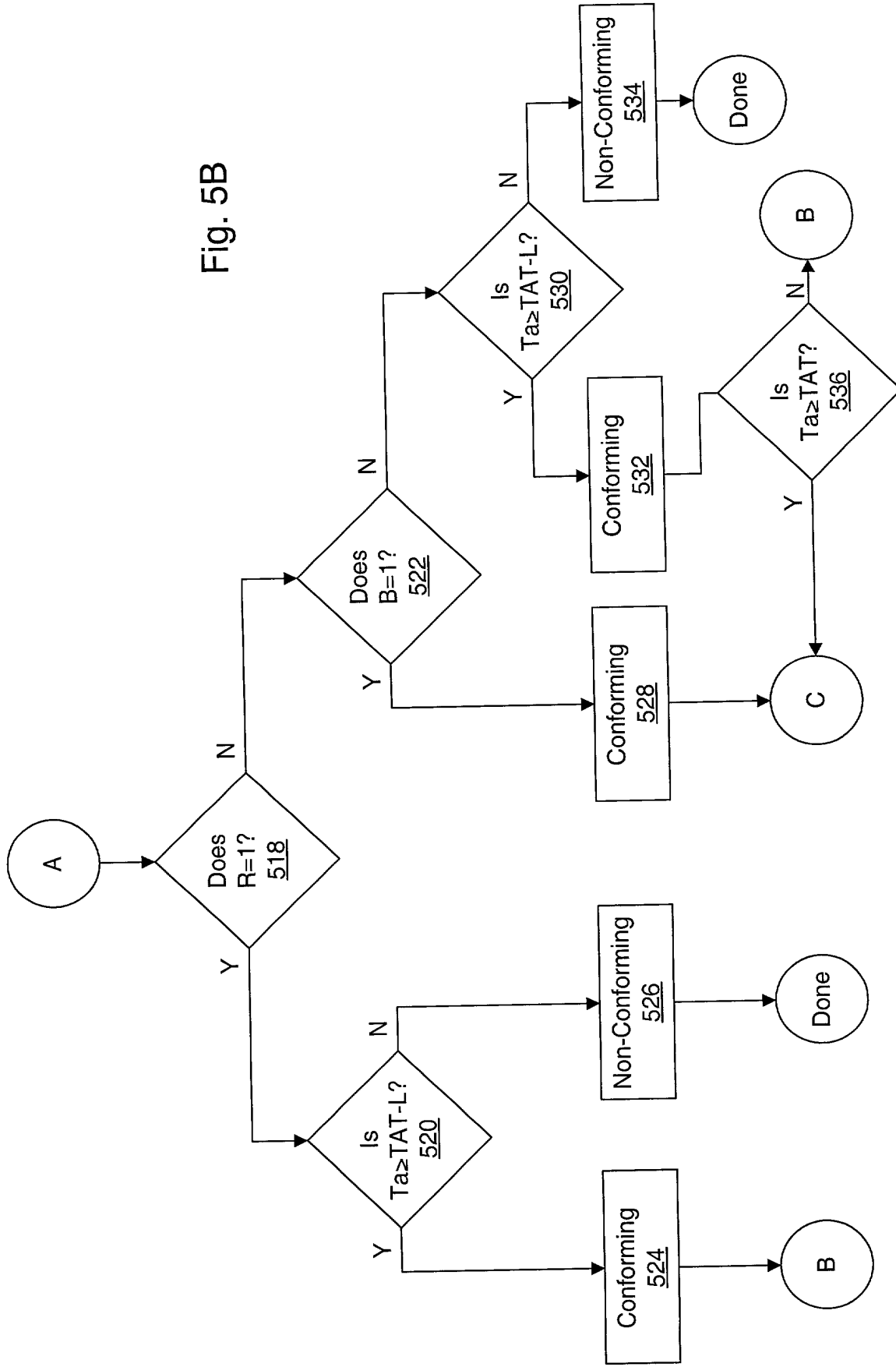


Fig. 5B

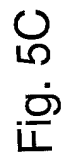


Fig. 5C

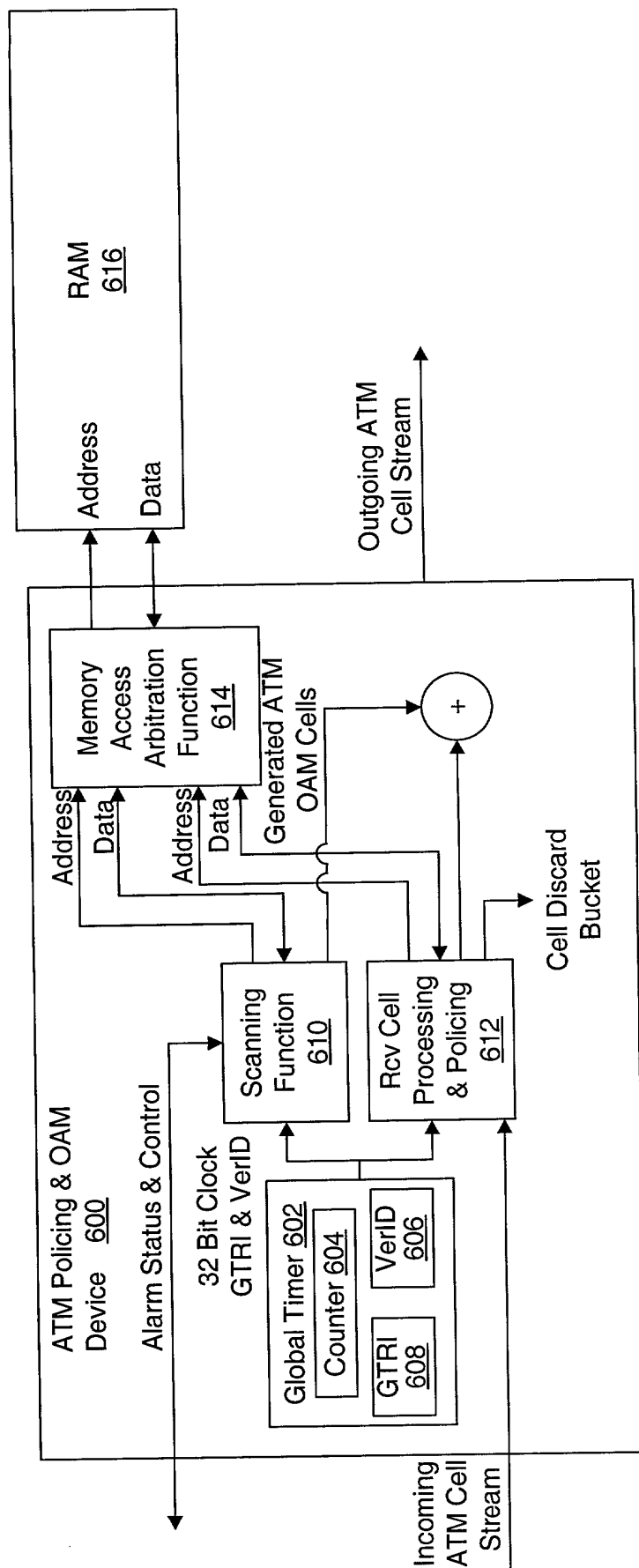


Fig. 6



702			704		706		700
VC# 1	Policing Parameter <sub>1</sub> Bits (includes TAT <sub>1</sub> , L <sub>1</sub> )	R-B-V <sub>1</sub> Bits	OAM Parameter <sub>1</sub> Bits (includes AIS, RDI, CC)				
VC# 2	Policing Parameter <sub>2</sub> Bits (includes TAT <sub>2</sub> , L <sub>2</sub> )	R-B-V <sub>2</sub> Bits	OAM Parameter <sub>2</sub> Bits (includes AIS, RDI, CC)				
VC# 3	Policing Parameter <sub>3</sub> Bits (includes TAT <sub>3</sub> , L <sub>3</sub> )	R-B-V <sub>3</sub> Bits	OAM Parameter <sub>3</sub> Bits (includes AIS, RDI, CC)				
•	•	•			•		
•	•	•			•		
•	•	•			•		
VC# 64K	Policing Parameter <sub>64K</sub> Bits (includes TAT <sub>64K</sub> , L <sub>64K</sub> )	R-B-V <sub>64K</sub>	OAM Parameter <sub>64K</sub> Bits (includes AIS, RDI, CC)				

Fig. 7

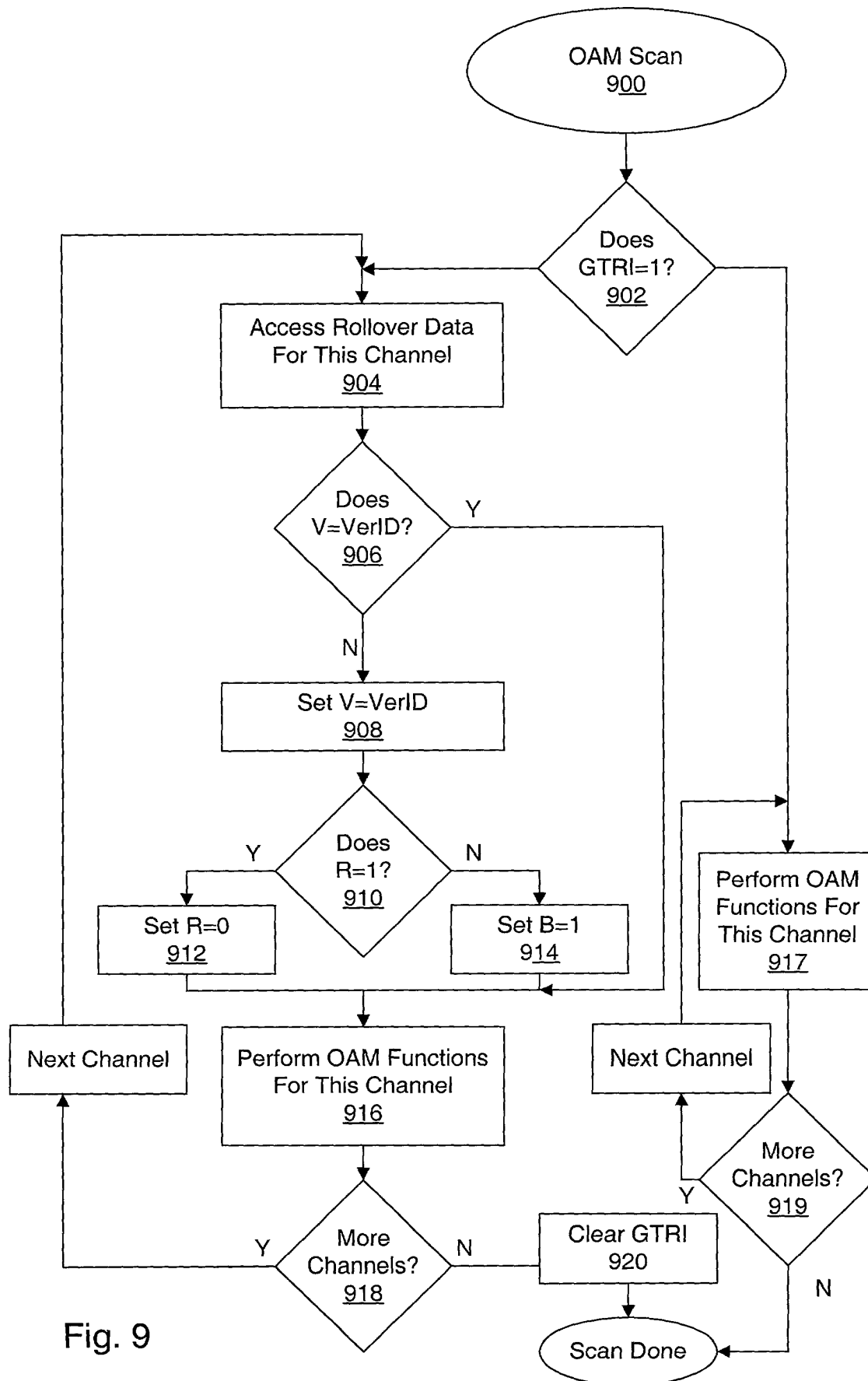


Fig. 9